



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Setsuo NAKAJIMA et al.                      Art Unit : 2812  
Serial No. : 09/768,618                                      Examiner : Stanetta Isaac  
Filed : January 25, 2001  
Title : SEMICONDUCTOR DEVICE AND ITS MANUFACTURING METHOD

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

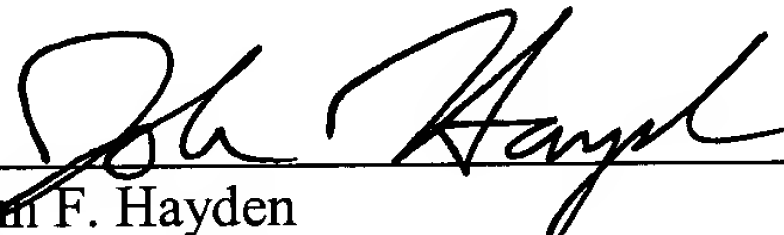
Copies of the references listed on the attached form PTO-1449 are enclosed.

This statement is being filed after a final Office action, but before payment of the issue fee. A check for \$180 in payment of the late submission fee of §1.17(p) is enclosed. Please apply any other charges or credits to Deposit Account No. 06-1050.

Respectfully submitted,

Date: \_\_\_\_\_

7/11/03

  
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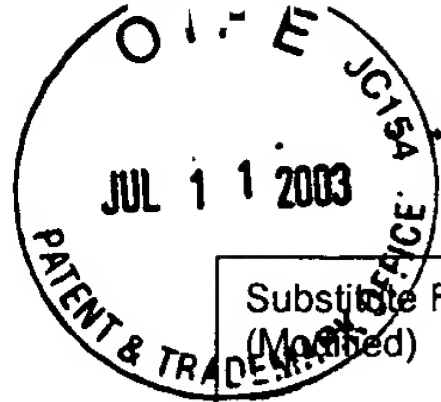
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Substitute Form PTO-1449 (Modified)	U.S. Department of Commerce Patent and Trademark Office	Attorney's Docket No. 12732-007001	Application No. 09/768,618
<b>Information Disclosure Statement by Applicant</b> (Use several sheets if necessary)  (37 CFR §1.98(b))		Applicant Setsuo NAKAJIMA et al.	
		Filing Date January 25, 2001	Group Art Unit 2812

U.S. Patent Documents							
Examiner Initial	Desig. ID	Document Number	Publication Date	Patentee	Class	Subclass	Filing Date If Appropriate
	AA	6,190,949	02/20/2001	Noguchi et al.			05/21/1997
	AB	6,096,581	08/01/2000	Zhang et al.			05/02/1996
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						
	AK						

Foreign Patent Documents or Published Foreign Patent Applications								
Examiner Initial	Desig. ID	Document Number	Publication Date	Country or Patent Office	Class	Subclass	Translation	
							Yes	No
	AL							
	AM							
	AN							
	AO							
	AP							

Other Documents (include Author, Title, Date, and Place of Publication)		
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	AQ	
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EXAMINER: Initials citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	